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a substrate for mounting said semiconductor element thereon to fix the side of a surface where the element is formed of said semiconductor element to one side thereof, said substrate being provided with a through hall formed from the one side to the other side of the substrate; and

2. A semiconductor package as claimed in claim 1, wherein said connecting pattern is provided continuously in a plurality of stages and an end portion of said connecting pattern on the side of said through hall is provided on a stage on the side of the one side of said substrate.

4. A semiconductor device with a semiconductor element fixed to a semiconductor package, said semiconductor package comprising:

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providing a substrate for mounting said semiconductor element thereon to fix said semiconductor element to one side thereof and a connecting pattern provided on the other side of said substrate and by forming a through hall from the one side to the other side of said substrate;

fixing a surface where the element is formed of said semiconductor element on the one side of said substrate of said semiconductor package such that an electrode of said semiconductor element is within said through hall;

electrically connecting said connecting pattern and said electrode of said semiconductor element via wires through said through hall; and

sealing said through hall and said wires with resin.

8. A method of manufacturing a semiconductor device as claimed in claim 7, wherein said connecting pattern is provided continuously in a plurality of stages and an end portion of said connecting pattern on the side of said through hall is provided on a stage on the side of the one side of said substrate.

9. A method of manufacturing a semiconductor device as claimed in claim 7, wherein said through hall is a plurality of through halls.

10. A method of manufacturing a semiconductor device as

claimed in claim 7, ~~8, or 9~~, wherein the surface where the element is formed of said semiconductor element is fixed on the one side of said substrate of said semiconductor package via a tape-like bonding material.

claimed in claim 7, ~~8, or 9~~, wherein the surface where the element is formed of said semiconductor element is fixed on the one side of said substrate of said semiconductor package with adhesive.

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